

FIG. 1

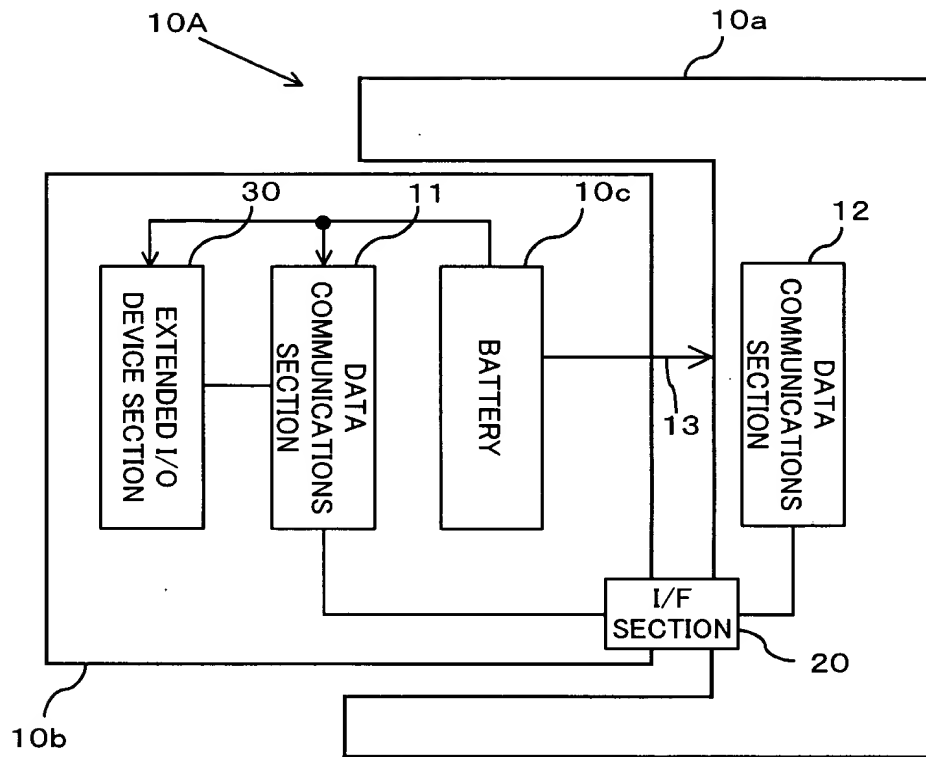
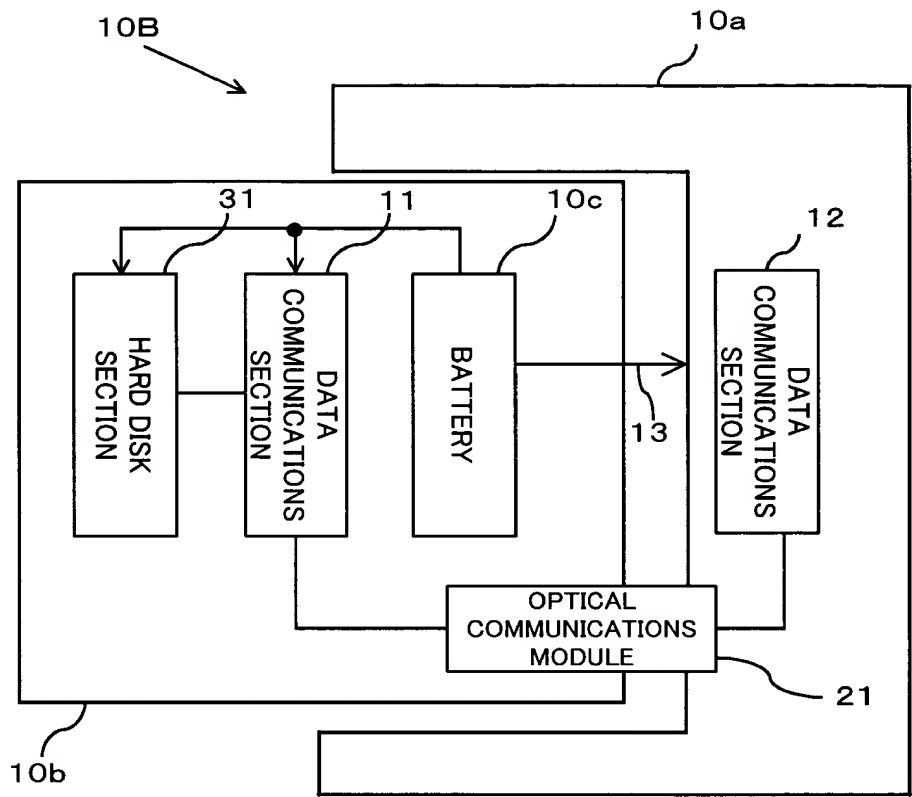


FIG. 2



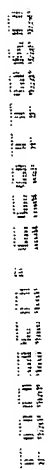


FIG. 4

FIG. 4

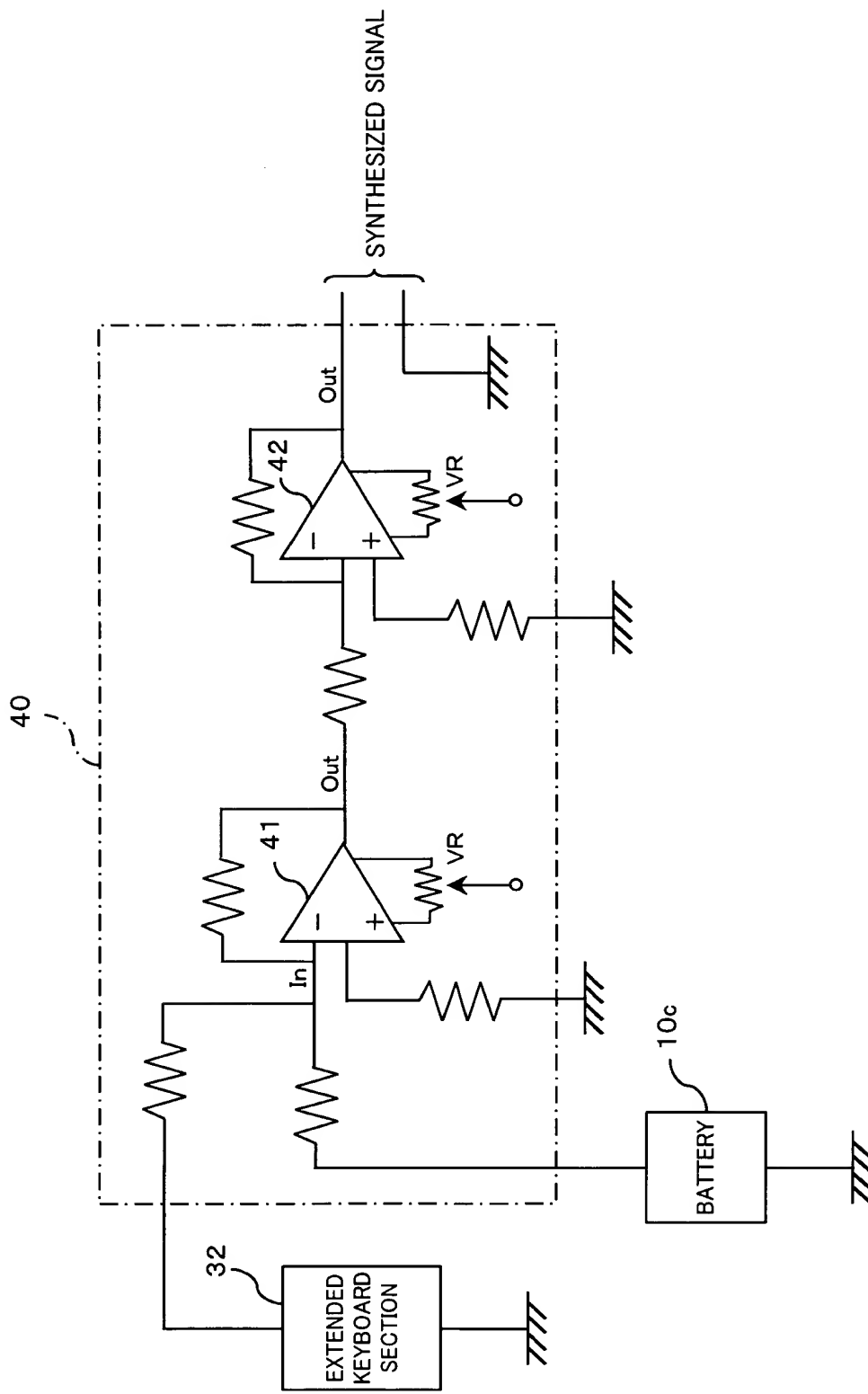
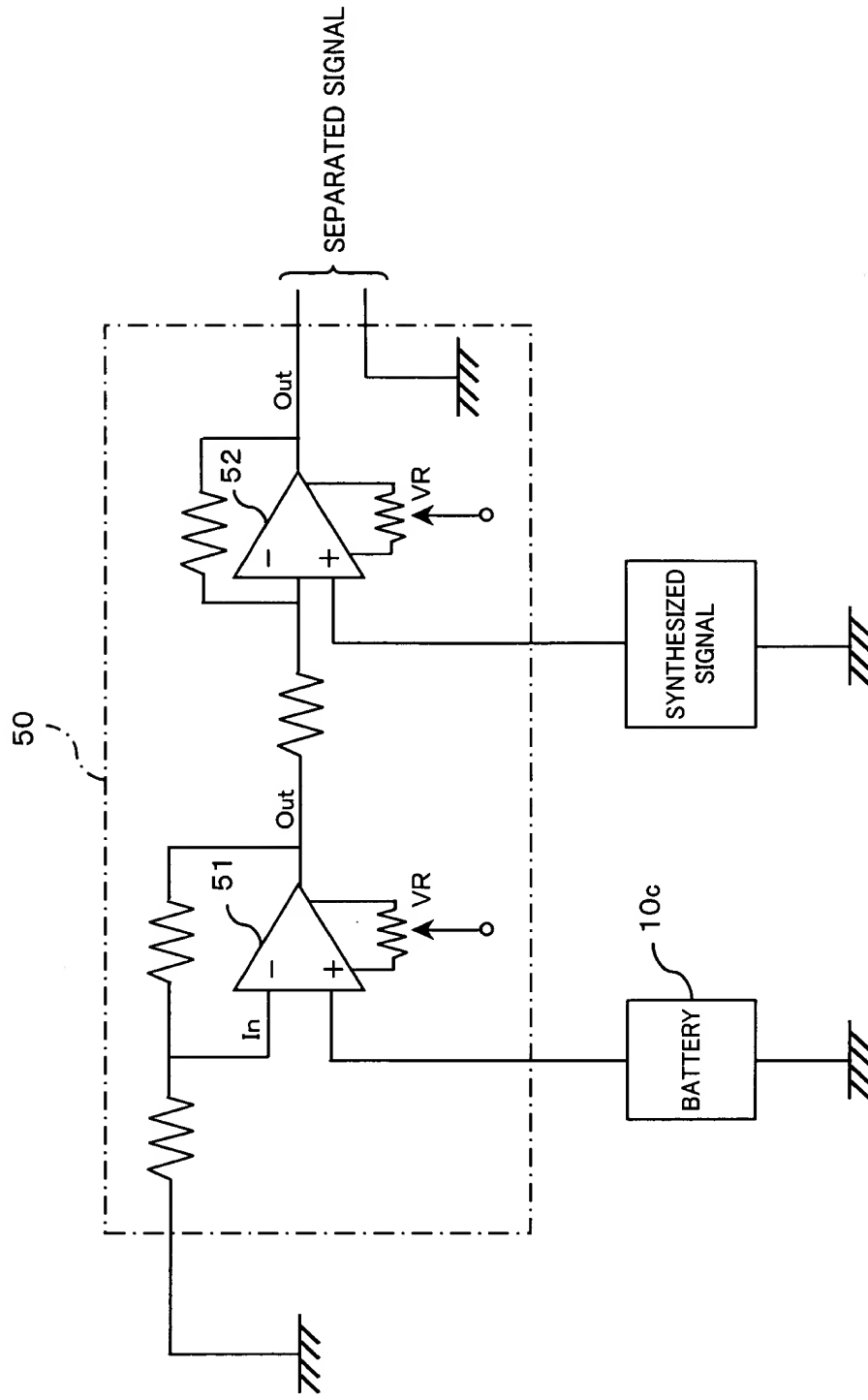


FIG. 5



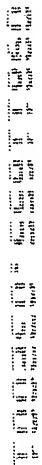


FIG. 7

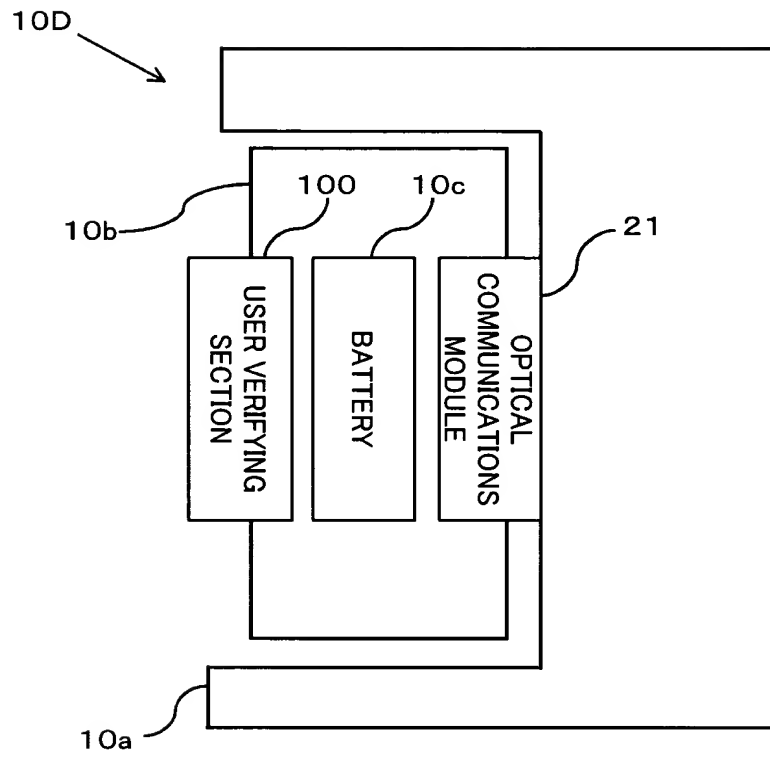


FIG. 8

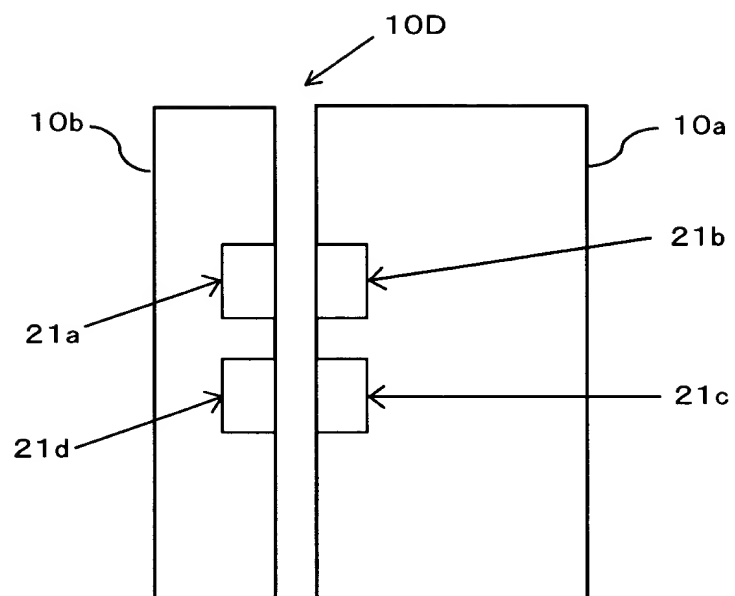


FIG. 9

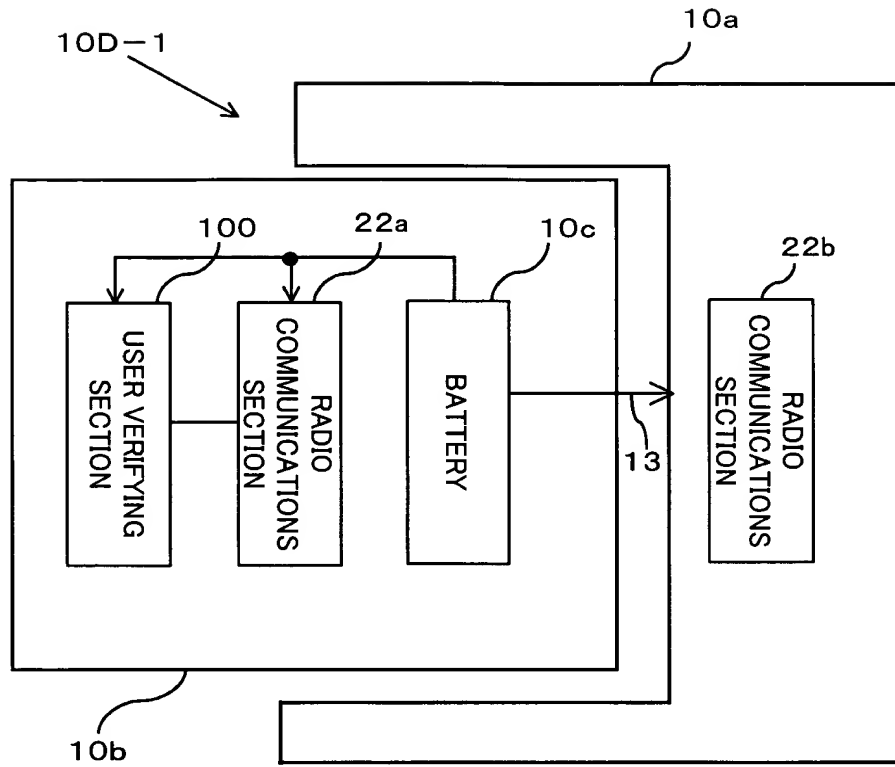


FIG. 9 is a block diagram of a device 10. The device 10 includes a sub-assembly 100, a radio communications section 22b, a battery, a user verifying section, and a radio communications section. The sub-assembly 100 includes a user verifying section, a radio communications section, and a battery. The battery is connected to a terminal 10c. The terminal 10c is connected to a switch 13. The switch 13 is controlled by the radio communications section 22b. The device 10 is connected to an external component 10D-1 via a connection point 10a. The sub-assembly 100 is connected to the main enclosure 10 via a connection point 10b.

FIG. 10

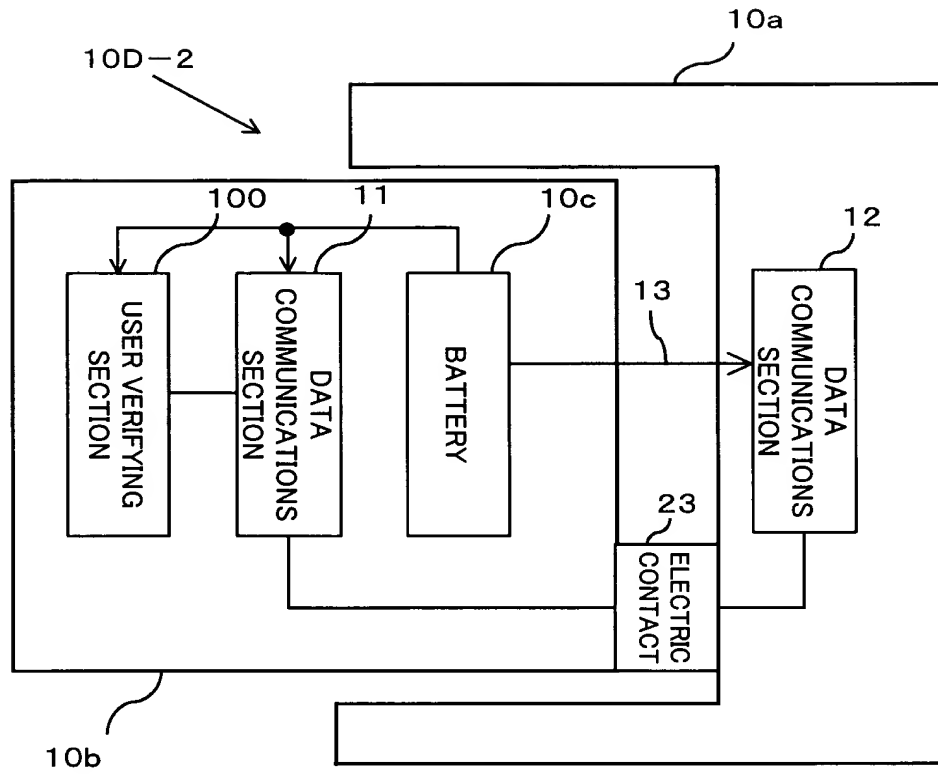
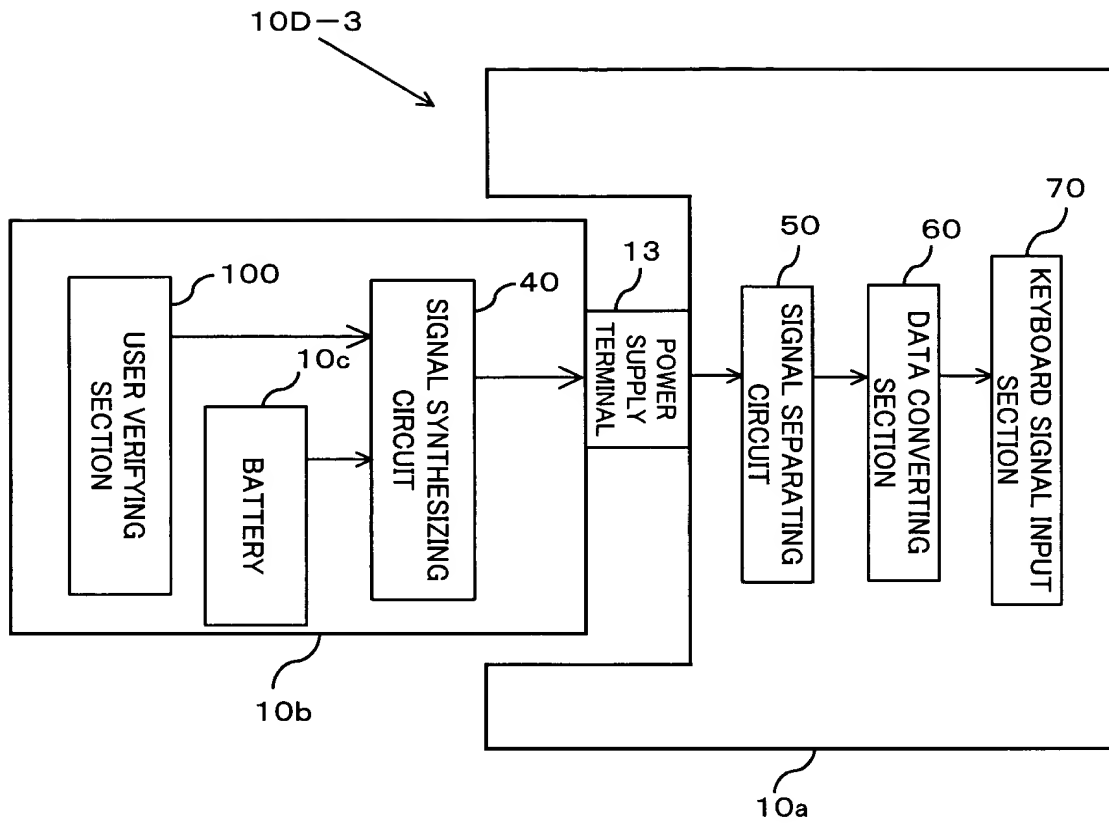


FIG. 10 is a block diagram of a device 10D-2. The device 10D-2 includes a top edge 10a and a bottom edge 10b. The device 10D-2 includes a central section 10c. The device 10D-2 includes a data communications section 12. The data communications section 12 is connected to a line 13. The line 13 is connected to an electric contact 23. The line 13 is connected to a battery 10c. The line 13 is connected to a data communications section 11. The line 13 is connected to a user verifying section 100.

FIG. 11



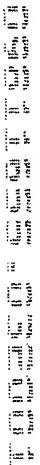


FIG. 13

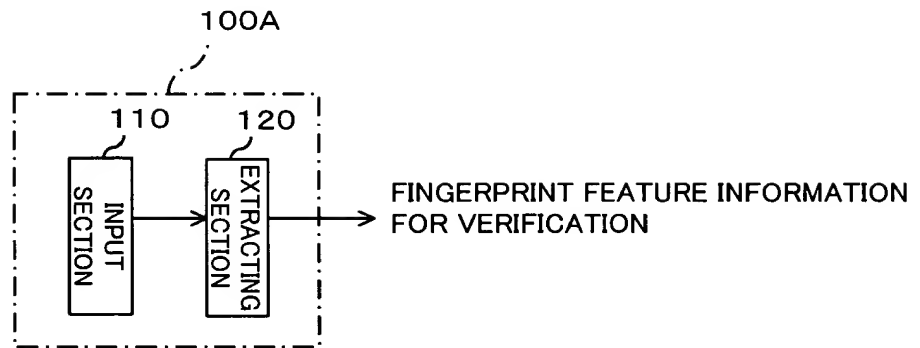


FIG. 14

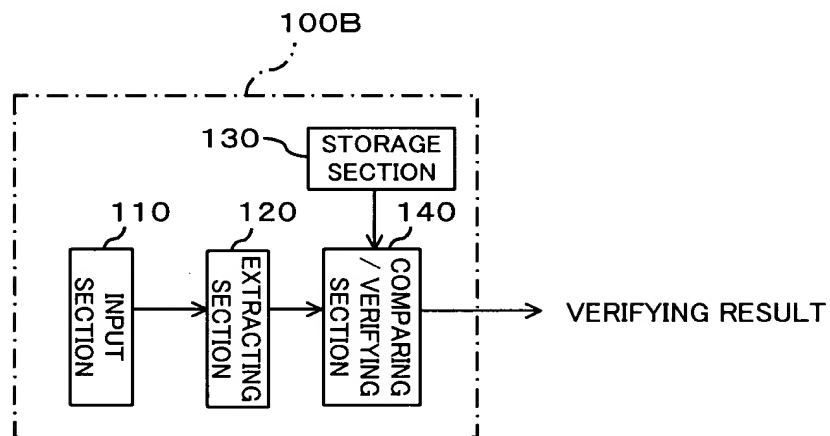


FIG. 15

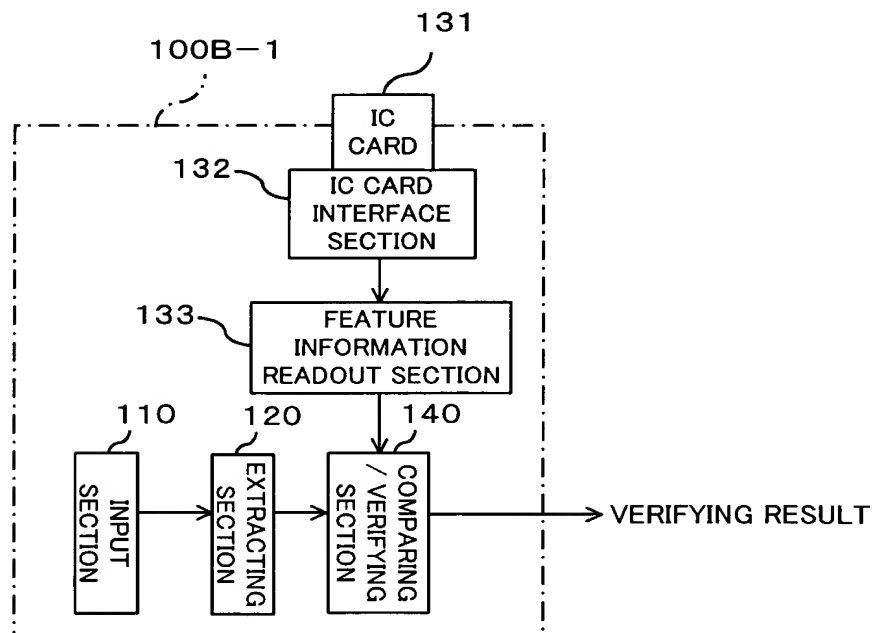


FIG. 16

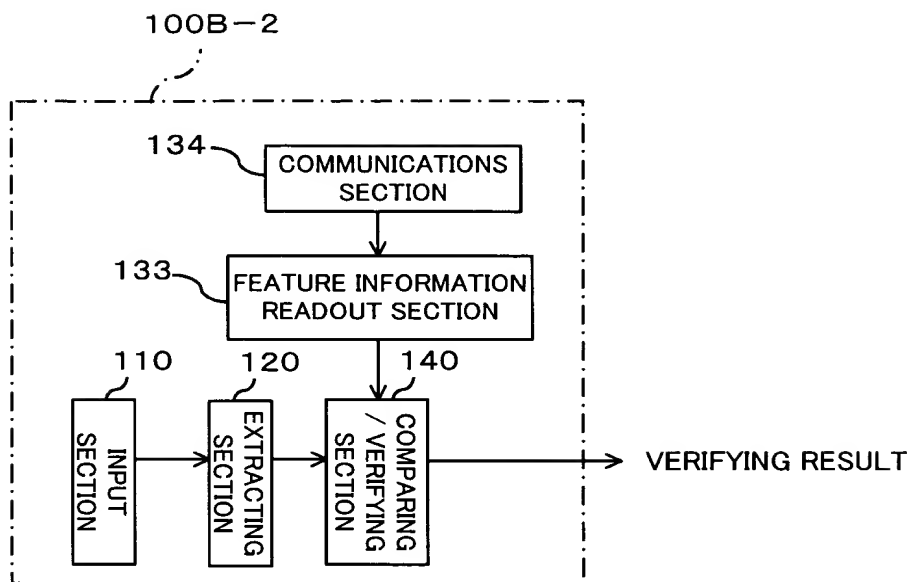


FIG. 17

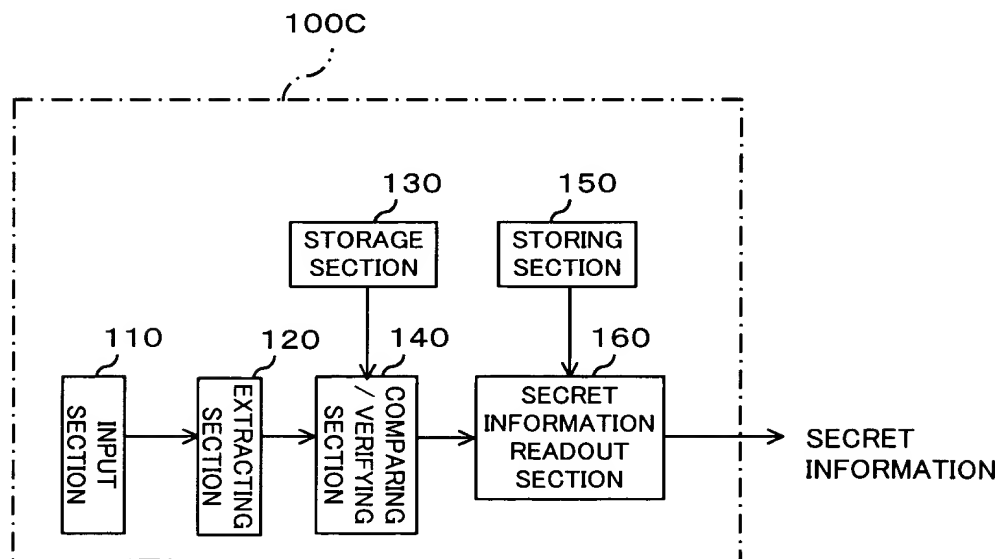


FIG. 18

